# Fast Turn-off Thyristor
## Type P0273SC12#

### Absolute Maximum Ratings

<table>
<thead>
<tr>
<th>VOLTAGE RATINGS</th>
<th>MAXIMUM LIMITS</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>V_{DRM} Repetitive peak off-state voltage, (note 1)</td>
<td>1200</td>
<td>V</td>
</tr>
<tr>
<td>V_{DSM} Non-repetitive peak off-state voltage, (note 1)</td>
<td>1200</td>
<td>V</td>
</tr>
<tr>
<td>V_{RRM} Repetitive peak reverse voltage, (note 1)</td>
<td>1200</td>
<td>V</td>
</tr>
<tr>
<td>V_{RSR} Non-repetitive peak reverse voltage, (note 1)</td>
<td>1300</td>
<td>V</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>OTHER RATINGS</th>
<th>MAXIMUM LIMITS</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>I_{(AV)} Mean on-state current, T_{sink}=55°C, (note 2)</td>
<td>273</td>
<td>A</td>
</tr>
<tr>
<td>I_{(AV)} Mean on-state current, T_{sink}=85°C, (note 2)</td>
<td>175</td>
<td>A</td>
</tr>
<tr>
<td>I_{(RMS)} Nominal RMS on-state current, T_{sink}=25°C, (note 2)</td>
<td>355</td>
<td>A</td>
</tr>
<tr>
<td>I_{(c,c.)} D.C. on-state current, T_{sink}=25°C, (note 4)</td>
<td>355</td>
<td>A</td>
</tr>
<tr>
<td>I_{SM} Peak non-repetitive surge t=10ms, V_{RM}=0.6V_{RRM}, (note 5)</td>
<td>3250</td>
<td>A</td>
</tr>
<tr>
<td>I_{SM2} Peak non-repetitive surge t=10ms, V_{RM}≤10V, (note 5)</td>
<td>3575</td>
<td>A</td>
</tr>
<tr>
<td>i²t Capacity for fusing t=10ms, V_{RM}=0.6V_{RRM}, (note 5)</td>
<td>52.8×10³</td>
<td>A²s</td>
</tr>
<tr>
<td>i²t Capacity for fusing t=10ms, V_{RM}≤10V, (note 5)</td>
<td>63.9×10³</td>
<td>A²s</td>
</tr>
<tr>
<td>(di/dt)cr Maximum rate of rise of on-state current (repetitive), (Note 6)</td>
<td>500</td>
<td>A/µs</td>
</tr>
<tr>
<td>(di/dt)cr Maximum rate of on-state current (non-repetitive), (Note 6)</td>
<td>1000</td>
<td>A/µs</td>
</tr>
<tr>
<td>V_{FGM} Peak forward gate voltage</td>
<td>12</td>
<td>V</td>
</tr>
<tr>
<td>V_{FM} Peak forward gate current</td>
<td>18</td>
<td>A</td>
</tr>
<tr>
<td>V_{RGM} Peak reverse gate voltage</td>
<td>5</td>
<td>V</td>
</tr>
<tr>
<td>P_{(AV)} Mean forward gate power</td>
<td>1.5</td>
<td>W</td>
</tr>
<tr>
<td>P_{GM} Peak forward gate power (100µs pulse width)</td>
<td>60</td>
<td>W</td>
</tr>
<tr>
<td>V_{GD} Non-trigger gate voltage, (Note 7)</td>
<td>0.25</td>
<td>V</td>
</tr>
<tr>
<td>T_{HS} Operating temperature range</td>
<td>-40 to +125</td>
<td>°C</td>
</tr>
<tr>
<td>T_{stg} Storage temperature range</td>
<td>-40 to +150</td>
<td>°C</td>
</tr>
</tbody>
</table>

**Notes:**
1) De-rating factor of 0.13% per °C is applicable for T_{j} below 25°C.
2) Double side cooled, single phase; 50Hz, 180° half-sinewave.
3) Single side cooled, single phase; 50Hz, 180° half-sinewave.
4) Double side cooled.
5) Half-sinewave, 125°C T_{j} initial.
6) V_{G}=80% V_{DRM}, I_{FG}=1A, t_{r}≤1µs, T_{case}=125°C.
7) Rated V_{DRM}.
## Characteristics

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>MIN.</th>
<th>TYP.</th>
<th>MAX.</th>
<th>TEST CONDITIONS (Note 1)</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{TM}$ Maximum peak on-state voltage</td>
<td>-</td>
<td>-</td>
<td>2.074</td>
<td>$I_{TM}=715\text{A}$</td>
<td>V</td>
</tr>
<tr>
<td>$V_{T0}$ Threshold voltage</td>
<td>-</td>
<td>-</td>
<td>1.55</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>$r_T$ Slope resistance</td>
<td>-</td>
<td>-</td>
<td>0.87</td>
<td></td>
<td>mΩ</td>
</tr>
<tr>
<td>$(\text{dv/dt})_{cr}$ Critical rate of rise of off-state voltage</td>
<td>200</td>
<td>-</td>
<td>-</td>
<td>$V_d=80%V_{DRM}$</td>
<td>V/µs</td>
</tr>
<tr>
<td>$I_{DRM}$ Peak off-state current</td>
<td>-</td>
<td>-</td>
<td>30</td>
<td>Rated $V_{DRM}$</td>
<td>mA</td>
</tr>
<tr>
<td>$I_{RRM}$ Peak reverse current</td>
<td>-</td>
<td>-</td>
<td>30</td>
<td>Rated $V_{RRM}$</td>
<td>mA</td>
</tr>
<tr>
<td>$V_{GT}$ Gate trigger voltage</td>
<td>-</td>
<td>-</td>
<td>3.0</td>
<td>$T_j=25\text{°C}$</td>
<td>V</td>
</tr>
<tr>
<td>$I_{GT}$ Gate trigger current</td>
<td>-</td>
<td>-</td>
<td>200</td>
<td>$T_j=25\text{°C}$, $V_d=6\text{V}, I_{T}=1\text{A}$</td>
<td>mA</td>
</tr>
<tr>
<td>$I_H$ Holding current</td>
<td>-</td>
<td>-</td>
<td>600</td>
<td>$T_j=25\text{°C}$</td>
<td>mA</td>
</tr>
<tr>
<td>$Q_{rr}$ Recovered charge</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>$I_{TM}=300\text{A}, t_p=500\mu\text{s}, dl/dt=20\text{A}/\mu\text{s}, V_r=50\text{V}$</td>
<td>µC</td>
</tr>
<tr>
<td>$Q_{ra}$ Recovered charge, 50% Chord</td>
<td>-</td>
<td>45</td>
<td>-</td>
<td></td>
<td>µC</td>
</tr>
<tr>
<td>$t_q$ Turn-off time (note 2)</td>
<td>25</td>
<td>-</td>
<td>40</td>
<td>$I_{TM}=300\text{A}, t_p=500\mu\text{s}, dl/dt=20\text{A}/\mu\text{s}, V_r=50\text{V}, V_d=80%V_{DRM}, dV_d/dt=20\text{V}/\mu\text{s}$</td>
<td>µs</td>
</tr>
<tr>
<td></td>
<td>20</td>
<td>-</td>
<td>30</td>
<td>$I_{TM}=300\text{A}, t_p=500\mu\text{s}, dl/dt=50\text{A}/\mu\text{s}, V_r=50\text{V}, V_d=80%V_{DRM}, dV_d/dt=200\text{V}/\mu\text{s}$</td>
<td>µs</td>
</tr>
<tr>
<td>$R_{thJC}$ Thermal resistance, junction to heatsink</td>
<td>-</td>
<td>-</td>
<td>0.12</td>
<td>Double side cooled</td>
<td>K/W</td>
</tr>
<tr>
<td>$F$ Mounting force</td>
<td>24.5</td>
<td>-</td>
<td>27</td>
<td></td>
<td>Nm</td>
</tr>
<tr>
<td>$W_t$ Weight</td>
<td>-</td>
<td>280</td>
<td>-</td>
<td></td>
<td>g</td>
</tr>
</tbody>
</table>

Notes:–
1) Unless otherwise indicated $T_j=125\text{°C}$.
2) The required $t_q$ (specified with $dV_d/dt=200\text{V}/\mu\text{s}$) is represented by an ‘#’ in the device part number. See ordering information for details of $t_q$ codes.
Notes on Ratings and Characteristics

1.0 Voltage Grade Table

<table>
<thead>
<tr>
<th>Voltage Grade</th>
<th>V_{DM} V_{DSM} V_{RRM} V_{RSM} V_D V_R</th>
</tr>
</thead>
<tbody>
<tr>
<td>12 DC V</td>
<td>1200 1300 810</td>
</tr>
</tbody>
</table>

2.0 Extension of Voltage Grades

This report is applicable to other and higher voltage grades when supply has been agreed by Sales/Production.

3.0 Extension of Turn-off Time

This Report is applicable to other t_q/re-applied dv/dt combinations when supply has been agreed by Sales/Production.

4.0 Repetitive dv/dt

Higher dv/dt selections are available up to 1000V/µs on request.

5.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for T_j below 25°C.

6.0 Rate of rise of on-state current

The maximum un-primed rate of rise of on-state current must not exceed 1500A/µs at any time during turn-on on a non-repetitive basis. For repetitive performance, the on-state rate of rise of current must not exceed 1000A/µs at any time during turn-on. Note that these values of rate of rise of current apply to the total device current including that from any local snubber network.

7.0 Square wave ratings

These ratings are given for load component rate of rise of forward current of 100 and 500 A/µs.

8.0 Duty cycle lines

The 100% duty cycle is represented on all the ratings by a straight line. Other duties can be included as parallel to the first.

9.0 Maximum Operating Frequency

The maximum operating frequency is set by the on-state duty, the time required for the thyristor to turn off (t_q) and for the off-state voltage to reach full value (t_v), i.e.

\[ f_{max} = \frac{1}{t_{pulse} + t_q + t_v} \]
10.0 On-State Energy per Pulse Characteristics

These curves enable rapid estimation of device dissipation to be obtained for conditions not covered by the frequency ratings.

Let $E_p$ be the Energy per pulse for a given current and pulse width, in joules
Let $R_{th(J-Hs)}$ be the steady-state d.c. thermal resistance (junction to sink)
and $T_{SINK}$ be the heat sink temperature.

Then the average dissipation will be:

$$W_{AV} = E_p \cdot f \quad \text{and} \quad T_{SINK(max.)} = 125 - \left( W_{AV} \cdot R_{th(J-Hs)} \right)$$

11.0 Reverse recovery ratings

(i) $Q_{ra}$ is based on 50% $I_{rm}$ chord as shown in Fig. 1 below.

![Fig. 1](image)

(ii) $Q_{rr}$ is based on a 100$\mu$s integration time.

i.e.

$$Q_{rr} = \int_{0}^{100\mu s} i_{rr} \cdot dt$$

(iii)

$$K \text{ Factor} = \frac{t1}{t2}$$

12.0 Reverse Recovery Loss

12.1 Determination by Measurement

From waveforms of recovery current obtained from a high frequency shunt (see Note 1, Page 5) and reverse voltage present during recovery, an instantaneous reverse recovery loss waveform must be constructed. Let the area under this waveform be $E$ joules per pulse. A new heat sink temperature can then be evaluated from:

$$T_{CASE(new)} = T_{CASE(original)} - E \cdot \left( k + f \cdot R_{th(J-Hs)} \right)$$

where $k = 0.227 \, (^\circ C/W)/s$

$E$ = Area under reverse loss waveform per pulse in joules (W.s.)
$f$ = rated frequency Hz at the original heat sink temperature.
$R_{th(J-Hs)}$ = d.c. thermal resistance ($^\circ C/W$).
The total dissipation is now given by:

$$W_{(TOT)} = W_{(original)} + E \cdot f$$

12.2 Determination without Measurement

In circumstances where it is not possible to measure voltage and current conditions, or for design purposes, the additional losses $E$ in joules may be estimated as follows.

Let $E$ be the value of energy per reverse cycle in joules (curves in Figure 9).
Let $f$ be the operating frequency in Hz

$$T_{CASE(new)} = T_{CASE(original)} - \left( E \cdot R_{th} \cdot f \right)$$

Where $T_{SINK(new)}$ is the required maximum heat sink temperature and $T_{SINK(original)}$ is the heat sink temperature given with the frequency ratings.

A suitable R-C snubber network is connected across the thyristor to restrict the transient reverse voltage to a peak value ($V_{rm}$) of 67% of the maximum grade. If a different grade is being used or $V_{rm}$ is other than 67% of Grade, the reverse loss may be approximated by a pro rata adjustment of the maximum value obtained from the curves.

12.3 Reverse Recovery Loss by Measurement

This thyristor has a low reverse recovered charge and peak reverse recovery current. When measuring the charge care must be taken to ensure that:

(a) a.c. coupled devices such as current transformers are not affected by prior passage of high amplitude forward current.
(b) A suitable, polarised, clipping circuit must be connected to the input of the measuring oscilloscope to avoid overloading the internal amplifiers by the relatively high amplitude forward current signal
(c) Measurement of reverse recovery waveform should be carried out with an appropriate critically damped snubber, connected across diode anode to cathode. The formula used for the calculation of this snubber is shown below:

$$R^2 = 4 \cdot \frac{V_r}{C_S \cdot \frac{di}{dt}}$$

Where: $V_r$ = Commutating source voltage
$C_S$ = Snubber capacitance
$R$ = Snubber resistance

13.0 Gate Drive

The recommended pulse gate drive is 20V, 20Ω with a short-circuit current rise time of not more than 1μs. This gate drive must be applied when using the full di/dt capability of the device.

The duration of pulse may need to be configured with respect to the application but should be no shorter than 20μs, otherwise an increase in pulse current could be needed to supply the resulting increase in charge to trigger.
Curves

Figure 1 - On-state characteristics of Limit device

Figure 2 - Transient thermal impedance
Figure 3 - Gate characteristics at 25°C junction temperature

Figure 4 - Gate trigger characteristic

Trigger point of all thyristors lie within the areas shown. Gate drive load line must lie outside appropriate $I_G/V_G$ rectangle
Figure 5 – Typical recovered charge

Figure 6 - Sine wave frequency ratings

Figure 7 - Sine wave frequency ratings

Figure 8 - Sine wave energy per pulse
Figure 9 - Square wave frequency ratings

Figure 10 - Square wave frequency ratings

Figure 11 - Square wave frequency ratings

Figure 12 - Square wave frequency ratings
Figure 13 - Square wave energy per pulse

Figure 14 - Square wave energy per pulse

Figure 15 – Maximum reverse recovery energy loss per pulse at $T_j = 125^\circ C$ and $V_{rm} = 804$ volts
Figure 16 - Maximum surge and I2t Ratings

- REVERSE VOLTAGE ACROSS THYRISTOR MAINTAINED DURING AND AFTER SURGE (gate may temporarily lose control of firing angle)

- (Itt: V_{RRM} = 0)
- (Itt: 60% V_{RRM})
- (I_{SM}: V_{RRM} = 0)
- (I_{SM}: 60% V_{RRM})

Total peak half-sine surge current, k. amperes

Duration of surge, m.secs

Maximum I2t (amps² secs)

- 10^6
- 10^4

- 10
- 1
- 0.1

Cycles at 50 Hz
Outline Drawing & Ordering Information

101A225

ORDERING INFORMATION  (Please quote 10 digit code as below)

<table>
<thead>
<tr>
<th>P0273</th>
<th>SC</th>
<th>♦ ♦ ♦ #</th>
</tr>
</thead>
<tbody>
<tr>
<td>Fixed Type Code</td>
<td>Fixed Outline Code</td>
<td>Off-state Voltage Code V_{DSS}/100</td>
</tr>
<tr>
<td>D=20µs, E=25µs, F=30µs</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Typical order code: P0273SC12E – 1200V V_{DSS}, 25µs t_q

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